

EDUCATION AND TRAINING

Researcher PhD Studies in Medical Devices

Technical University "Gheorghe Asachi" [30/09/2019 – Current]

City: Iași

Country: Romania

Field(s) of study: Electrical Engineering

MSc in Embedded Computer Systems

Faculty of Automatic Control and Computer Science, Technical University "Gheorghe Asachi" [2009 – 2011]

City: Iași

Country: Romania

Field(s) of study: Information and Communication Technologies

Final grade: 10

BSc in Computer Science

Faculty of Automatic Control and Computer Science, Technical University "Gheorghe Asachi" [2004 – 2009]

City: Iași

Country: Romania

Field(s) of study: Information and Communication Technologies

Final grade: 9,75

Secondary Education in Mathematics and Computer Science

"Ștefan Procopiu" High School [1999 – 2004]

City: Vaslui

Country: Romania

Field(s) of study: Information and Communication Technologies

Final grade: 9,69

WORK EXPERIENCE

Senior System Software Engineer (Contract Based)

Mechatronics Innovation Center SRL [24/06/2018 – Current]

City: Iași

Country: Romania

The main tasks are concentrated on developing firmware, elaborating the planning and writing test cases for unit and integration testing of firmware applications for medical devices in line with product development process according to ISO 13485 and medical software development according to IEC 62304. The projects working on were for various medical devices from class C to class A software categories or class 3 to class 1 system categories. Some additional tasks consist of analyzing hardware circuits and debugging eventual hardware faults of various medical devices projects. During some of the projects there were technical management assignments fulfilled along with some regulatory tasks regarding medical devices development. For firmware and software development the work experience gained was on various environments, like Visual Studio, Eclipse, UNIX based IDEs combined with various toolchains and compilers (e.g., CMake, GCC, ARM-GCC and PC-Lint, etc.) to build and configure the targets for debug and release binaries and testing. Regarding the tasks assignments, bug tracking and versioning systems, Jira, Confluence, GitHub and SVN combinations were used. Among the additional activities there were requests in supporting various software tools validation tasks for the medical devices' development process.

Presilicon Integration and Hardware Design Verification Engineer

Silicon Service SRL (AMD Contractor) [14/02/2011 – 19/06/2018]

City: Iași

Country: Romania

Taking part in Hardware Design Verification projects using UVM, OVM, Verilog/VHDL, System Verilog, C++, and assembly language. At the beginning the tasks for this job were verifying x86 and ARM processor designs, debugging various issues appearing in both RTL and verification environment, offering solutions and fixes and fulfilling integration/merging tasks at SoC and IP level by using versioning systems (i.e. Perforce). Also had responsibilities in running test regressions, collecting and generating coverage, maintaining and upgrading the verification and testing environment as needed by using scripting languages (i.e. Perl, bash, csh). In the last three years I had consistent contribution in developing and adding new features to the verification environment described in both C++ and System Verilog/UVM using mostly UNIX environment for compiling and run-time tasks. Also, during this job there was the first contact in gaining experience with Agile and Scrum oriented methodologies. For bugs and enhancement tracking and management Jira was intensively used in all the projects.

System Network Administrator (Contract Based)

Mechatronics Innovation Center SRL [2018 – Current]

City: Iași

Country: Romania

Providing support for the computer network and being charge of maintaining the network and data security of the company.

System Network Administrator (Contract Based)

Radani SRL [2003 – Current]

City: Vaslui

Country: Romania

Providing support for the computer network and being charge of maintaining the network and data security of the company.

Associate Teacher in Embedded Computers Laboratory

Faculty of Automatic Control and Computer Science, Technical University "Gh. Asachi" Iași [2009 – 2010]

City: Iași

Country: Romania

Teaching students about programming using microprocessors during laboratory classes. The classes consisted of learning how to assembly a development board using Microchip PIC16F876A and develop various applications to interact with sensors and actuators using embedded C and Microchip tools.

Teacher in Robotics

"Ștefan Procopiu" High School Vaslui, Romania [2009 – 2010]

City: Vaslui

Country: Romania

Organizing training courses for students in terms of construction and programming of mobile systems with microcontrollers during the Center of Excellence in Robotics classes. The microcontrollers technologies used were from both Microchip and Atmel and were programmed using C++, but also assembly language for various applications. There were also used various development boards, sensors and actuators.

LANGUAGE SKILLS

Mother tongue(s): **Romanian**

Other language(s):

English

franceza

LISTENING C2 READING C2 WRITING C2

LISTENING A2 READING A2 WRITING B1

SPOKEN PRODUCTION C2 SPOKEN INTERACTION C2 SPOKEN PRODUCTION A2 SPOKEN INTERACTION A2

Spanish

LISTENING A2 READING A2 WRITING A2

SPOKEN PRODUCTION A2 SPOKEN INTERACTION A2

Levels: A1 and A2: Basic user; B1 and B2: Independent user; C1 and C2: Proficient user

SKILLS ACHIEVED AT THE WORK PLACE

Computing skills

Knowledge in programming languages: very good knowledge of C, embedded C, C++;

Some knowledge in developing Unix/Linux related scripts by using Perl, bash, csh;

Knowledge in using and configuring various toolchains and compilers like gcc, arm-gcc, cmake;

Knowledge in hardware description language using sVerilog, VHDL;

Knowledge in using versioning systems and tools like Perforce, SVN, GitHub;

Good knowledge in using bug tracking engines like Bugzilla and the more recent tool Jira.

Technical Skills

Experience in designing, programming and debugging embedded systems endowed with ARM based microcontrollers (e.g., STM32, NXP, Atmel, Microchip, Renesas, etc.).