

# Software Verification Engineer

## Contact

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## Career Summary

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2016– Present

### Validation Engineer, Intel Technology Poland

- Developing frameworks and test scripts in multiple environments (different OSes and Pre-OS)
- Managing automation team in agile methodology
- Developing tools and solutions in C language.
- Scripting build systems and tests in python and within bash and powershell.
- Close coordination with product management and coordinating releases
- Designing project documentation and product specification
- Direct contact with prototypes hardware and rack scale solutions
- Experience with simulation and emulation environment
- Responsibilities to handle extensive virtual infrastructure
- Working with x86, x64 and ARM architectures and knowledge of specification of those systems
- Firmware related validation Validation of SMART NIC firmware, software and practical application

2012-2016

### Development Engineer, Intel Technology Poland

- Developing and enhancement of option roms and UNDI drivers in legacy and UEFI environment
- Debugging and fixing client specific bugs and close cooperation with clients
- Working with prototype hardware & software and resolving architectural challenges
- Extensive usage of assembler, C oraz C#

## Education

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### Automation and robotics, Gdansk University of Technology

2008-2012

### Microelectronics, Gdansk University of Technology

2012-2014

**Other Courses:** ISTQB, Test Manager level | Professional Scrum Master I (PSM I )

## Skills

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**Tech:** C, C++, C#, Java, Python, Bash, Powershell, REST API, SQL Git, Github, Perforce, SVN, Jira, CI/CD, Pytest x86, x64, ARM Agile; kanban, scrum, PowerBi Docker, qemu Server/network management, Cisco, Mellanox, Dell, HP, Supermicro, Lenovo etc

## Languages

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**English:** Fluent

**French:** Basic